

IN THE CLAIMS

Please amend the claims as follows:

1 49. (Amended) A method of controlling a temperature of a
2 microprocessor, [wherein the microprocessor performs] comprising the steps
3 of:
4 a) generating a temperature signal within the microprocessor
5 indicative of the temperature of the microprocessor;
6 b) comparing the temperature signal with a first threshold
7 temperature level within the microprocessor;
8 c) generating an interrupt signal if the temperature signal indicates
9 that the first threshold temperature level has been exceeded; and
10 d) decreasing a microprocessor clock frequency in response to the
11 interrupt signal.

1 51. (Amended) A method of controlling a temperature of a
2 microprocessor, [wherein the microprocessor performs] comprising the steps
3 of:
4 a) generating a temperature signal within the microprocessor
5 corresponding to the temperature of the microprocessor;
6 b) comparing the temperature signal with a first threshold
7 temperature level within the microprocessor;
8 c) generating an interrupt signal if the temperature signal indicates
9 that the first threshold temperature level has been exceeded; and

10 d) activating an active cooling device to decrease the
11 microprocessor temperature in response to the interrupt signal.

In claim 54, line 3, after "signal" insert --within the microprocessor--.

1 58. (Amended) The method of claim 54 wherein step d) further comprises the
2 steps of:
3 i) driving the clock signal at a first frequency if neither the
4 first threshold signal nor the second threshold signal are asserted; and
5 ii) driving the clock signal at a second frequency if the first
6 threshold signal is asserted and the second threshold signal is not asserted,
7 wherein the second frequency is less than the first frequency.
8 [step of driving the clock signal at an intermediate frequency if the second
9 threshold signal is asserted and the first threshold signal is deasserted.]